

TC-720

Subclass	Class

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**PATENT NUMBER**

**U.S. UTILITY Patent Application**

**O.I.P.E**

**PATENT DATE**

7-11  
SCANNED Dec 1 Q.A. GH

APPLICATION NO. 09/681103	CONT/PRIOR	CLASS 716 703	SUBCLASS 24	ART-UNIT 2025 2123	EXAMINER JONES BROOK HOGAN
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ENTERTAINMENT

avrLogic, emulator with routing chip providing virtual full-crossbar interconnect.

PTO-2040  
12/89

## **ISSUING CLASSIFICATION**

Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				<b>NOTICE OF ALLOWANCE MAILED</b>	
	(Assistant Examiner) _____ (Date) _____				
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				<b>ISSUE FEE</b>	
	(Primary Examiner) _____ (Date) _____			Amount Due	Date Paid
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